Design the memory map and memory decoder for a 16 bits microprocessor (20 address lines) system using the following memory requirements:

- 128KB ROM, using 64K x 16 bits memories, at the end

- 256KB SRAM, using 64K x 16 bits memories

- 512KB DRAM, using 256K x 16 bits memories

* Nr of circuits
* Circuit size
* Memory Map

1. Step 1 – above
2. Step 2 – Blocks
3. Step 3 – Block Sizes
4. Step 4 – Memory Map

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